# Pintos @verview

— Why, What and How

TA Session



TA: xiangyuxing 向昱行

Email: echostone@stu.pku.edu.cn

GitHub: EchoStone1101

#### Some announcements:

#### **Getting right started...**

- ➤ Lab 0 is released this Tuesday
- ➤ Lab 0 Code will be due next Thursday 11:59 pm
- ➤ Lab 0 Design Doc will due next Sunday 11:59 pm

The rest Labs have similar deadlines...



#### Educational OS Project Zoo



JOS

IA32 MIT6.828 xv6

RISCV32 MIT6.S081



Nachos

MIPS Old CS162



JOHNS HOPKINS

UNIVERSITY

IA32

Stanford

University

CS162





#### Design and Implementation





➤ OSDI, NSDI, PLDI ... ...

> Talk is cheap, show me the code

> Your design matters !!

➤ Write 2000+ LOC in a 10000+ LOC codebase



#### You will learn by Read The Code

- > important skill both in production and research
- > learn from good coding style
- > some tools may help you



#### You will learn by Design The Code

- > think tenth, code once
- > design doc template may help you
- > not Pintos, but Your Pintos



#### You will learn by Write The Code

- > maybe your first time writing 2000+ LOC
- > tricky multi-threading synchronization
- > test-driven development



#### Me, debugging

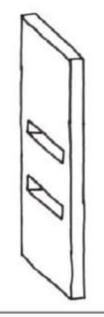
Q:Why

You wil

> You will

> start earl

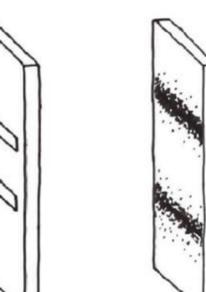






Code











#### Q: Why not Pintos?

> IA32 architecture : CISC ISA, historical legacy





Pintos reimplemented in Rust based on RISCV64.



#### Q: Why not Pintos?

➤ IA32 architecture : CISC ISA, historical legacy

Pintos Tacos

time consuming: 100 hours +++
optional lab4, long long long lab document, per-lab TA session



Q: So ... what will you do?

P2: System Call Layer P1:Alarm Clock P2: Process Management P2: File Management P1:MLFQS P3b: Mmap Eilo P3a: Page P3a: P4: Hierarchical Fault Demand P1: Priority Multi-threaded Handler Paging Donation Filesystem Physical P1: Priority MMU Basic Memory Scheduler Support Filesystem Manager Simple FIFO Scheduler **Device Support** Keyboard, VGA, Serial Port, Timer, PCI, IDE P0: Boot Support **Pintos Kernel** 

P0: Getting Real

P1: Threading

P2: User Programs

P3: Virtual Memory

P4: File System

Students Create

**Support Code** 

## Typical workflow:

Lab released on the Course Website



Read through the lab document



We are here for Lab 0!



Read through the lab document and design doc carefully



Design your data structures and interfaces

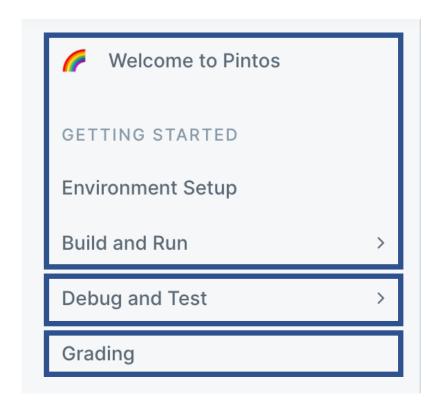


Write and Debug code
Pass all the test cases
Submit code zip before code DDL



Answer the questions in design doc submit it before design doc DDL

#### Q: How to survive? PintosBook long, but helpful



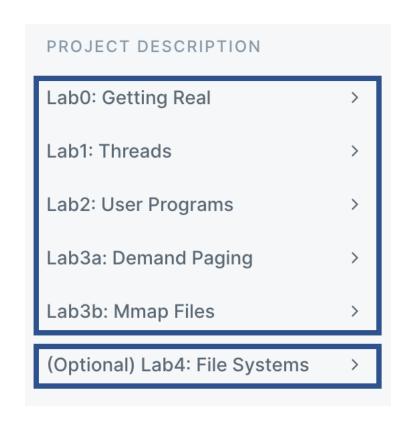
Set up your local development environment.

Look through it and look back if needed.

Important, read it carefully.



#### Q: How to survive? PintosBook



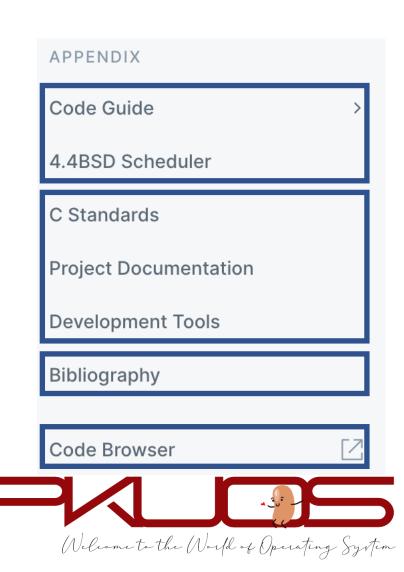
Look through it before each TA Session.

Read it carefully during implementation.

Optional but rewarding Lab4.



#### Q: How to survive? PintosBook





#### Q: How to survive? Your kind TA

#### Learn to ask questions.

Do not be shy, ask in class, in office hour or in the Piazza.

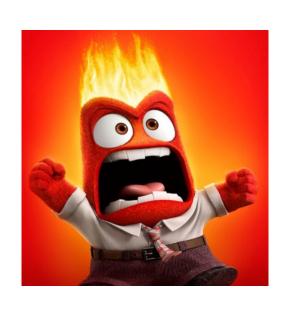
Office hour: Friday 10:00-11:00 a.m., Yan Yuan Building 818

Piazza: https://piazza.com/pku.edu.cn/spring2025/04834490

(see course page)



## But ... ... your TAs are not your <del>personal assistants (or Mr.Deepseek)</del>.



- "My program crashed."
- "What does this error mean?"
- "I failed xxx testcase."
- "My computer can not boot."



#### Think twice, Ask once.

➤ How to ask questions the smart way.

> RTFM

> STFW



#### Think twice, Ask once.



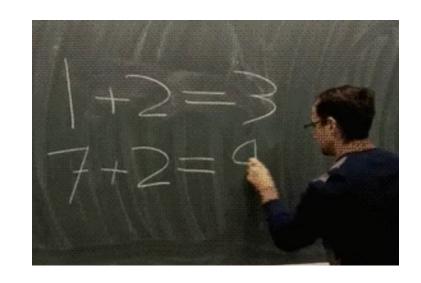
- "I encounter xxx under xxx condition."
- "Google says xxx, StackOverflow says xxx, Document says xxx, but yyy."
- "Hey, TA, I found xxx and I think you do not know about it!"



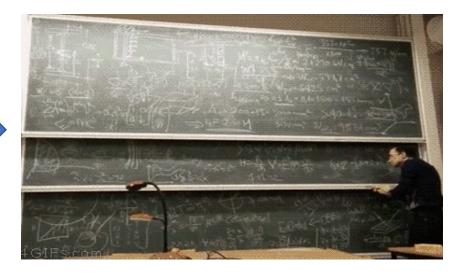
#### Q: How to survive? Good habits

awkward, but helpful

#### Use Version Control tool — Git



A week later



Newly written code

The same code





How to write good commit message.

#### Q: How to survive? Good habits

#### Write Concise but good comments.

- > Summarize the function in one sentence first.
- Pre-condition: input constraints (You may ASSERT these constraints)
- > Post-condition: return value, exception (kernel panic)



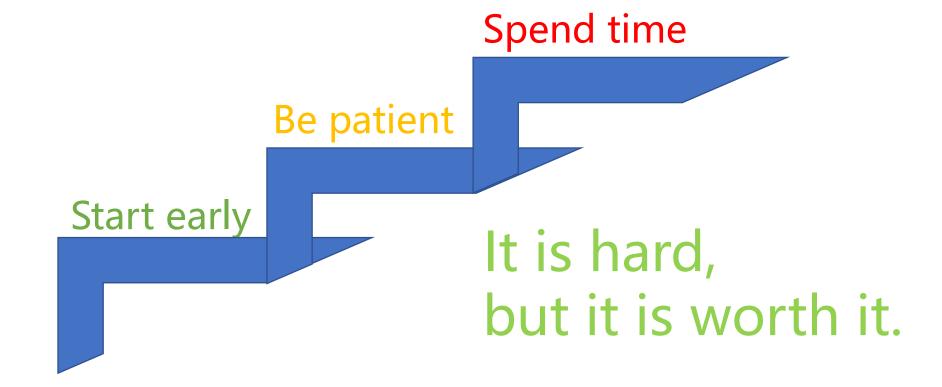
#### Q: How to survive? Good habits

#### Module and Abstraction.

- > A function should (only) do one thing clean
- A function more than 100 LOC warning
- > A function more than 200 LOC Something may go wrong



#### Q:How to survive?





# Lab0 FAQs



#### In this Lab, you will be...

- > Walking through the **booting** of Pintos
- > Try your hands on **debugging** Pintos
- Write your first line of code in Pintos: a tiny shell

#### **OS Booting sounds overwhelming?**

- All essential information are provided in the PintosBook
- You don't need to master all details; you practice how you learn from new information!



#### Cutting through the confusing jargons...

#### You will (assuming you use Docker for running Pintos):

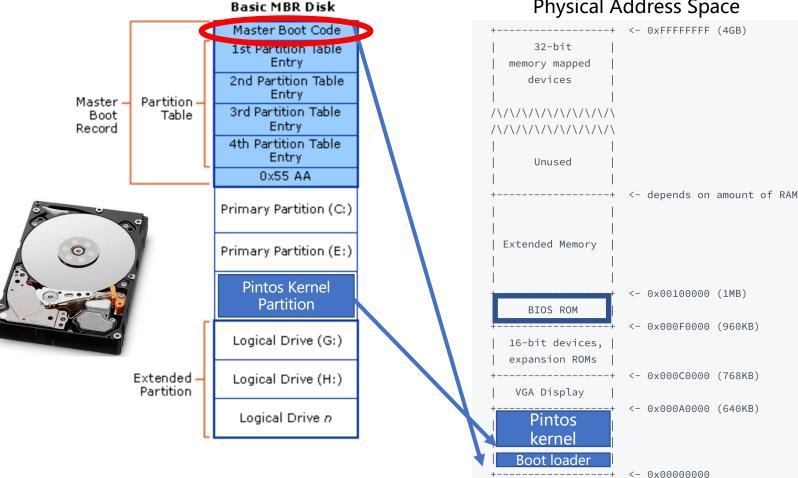
- > develop Pintos in a ubuntu:18.04 container (regardless of your host)
- cross-compile Pintos into i386 (Intel 80386) binaries (IA32, 4GB) This architecture is hopefully familiar - it was discussed in ICS.
- > execute Pintos with either QEMU or Bochs, kernel emumators
  - > ...which come with the BIOS (Basic Input/Output System) firmware that loads a custom OS **bootloader**
- Ioader.S ...which, as part of Pintos, locates and loads the actual **kernel** ...which is nothing but an i386 ELF executable, with .text, .data,
  - ...which is nothing but an i386 ELF executable, with .text, .data, and an entry point
    - > ...which switches from real mode to protected mode, and calls pintos init()

start.S

Booting Pintos

This MBR code is usually referred to as a boot loader.

Physical Address Space





Hard-wired by the hardware

The real-world booting process can be much more complicated

GRUB (GRand Unified Bootloader), UEFI (Unified Extensible Firmware Interface), ...



#### X86 Mode (history legacy)

X86 Real Mode

**Enabled in start.S** 

X86 Protected Mode

➤ 16-bit Instructions and Registers

AX, BX, CX, DX, SI, DI, BP, SP

20-bit Memory Address Space (Up to 1MB)

16-bit segment registers

CS, DS, SS, ES, FS, GS

PAddr = SEG << 4 + Operand

32-bit Instructions and Registers
EAX, EBX, ECX, EDX, ESI, EDI, EBP, ESP

> 32-bit Memory Address Space (Up to 4GB)

Reserved segment registers, but for protection

Address translation enabled



#### And if you really want to know every detail...

- ➤ Dig through Makefiles for how the Bootloader (loader.S) and the kernel itself is linked and run
- >src/utils/pintos is how you will be running Pintos, which is actually a Perl script that you can try to read and understand



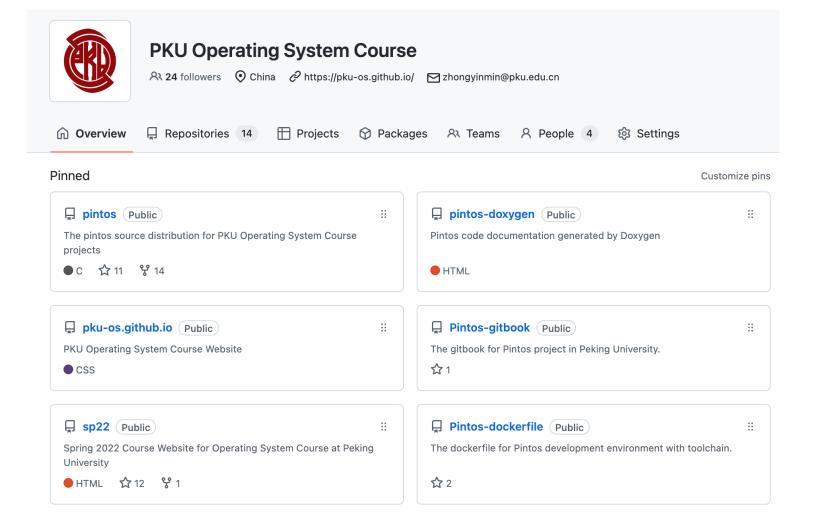
#### Conclusion

- ➤ Why Pintos?
  - Design and Implementation
  - Read, Design, Write, Debug the code
- > What will you do in the projects?
  - Projects Map
  - Typical workflow
- ➤ How to survive the projects?
  - PintosBook
  - Ask questions
  - Good habits
  - Good attitude
- ➤ Lab0 FAQs: Booting Pintos, X86 mode





#### https://github.com/PKU-OS



# Learn it, Master it, Love it, and Join us.

**Email:** <a href="mailto:echostone@stu.pku.edu.cn">echostone@stu.pku.edu.cn</a></a>
<a href="mailto:GitHub:EchoStone1101">GitHub:EchoStone1101</a>